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### **Diode Detectors For Rf Measurement Part 1: Rectifier ...**

2 diode detectors for rf measurement part 1: rectifier circuits, theory and calculation procedures. table of contents abstract.....1

### **Antenna Circuit Design For Rfid Applications**

an710 ds00710c-page 2 2003 microchip technology inc. the magnetic field produced by a circular loop antenna is given by: equation 3: where the above equation indicates that the magnetic field

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### **Virtuoso Spectre Circuit Simulator Reference - Ampic Lab**

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scu center for analog design and research microstrip patch antenna design principles ben horwath

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kit 32. three stage fm transmitter radio frequency choke) and the 10p capacitor in parallel with it are designed to reduce harmonics from the circuit.

### **Designing Gain And Offset In Thirty Seconds - Ti.com**

sloa097 4 designing gain and offset in thirty seconds r1 vref' vout rg2 vref++ rg1 rf vin figure 2. schematic diagram for positive m and negative b tuning this is the point at which the approximation accumulates errors.

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**Neural Network Structures - leee**

64 neural networks for rf and microwave design table 3.1 a comparison of modeling approaches for rf/microwave applications empirical and basis for equivalent circuit pure neural network

**6 Ghz To 26 Ghz, Gaas Mmic Fundamental Mixer Data Sheet ...**

specifications electrical specifications  $t_a = 25$ ,  $f = 500$  mhz,  $lo$  drive = 13 dbm, rf frequency range = 6.0 ghz to 16.0 ghz, all measurements performed as a

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international journal of scientific and research publications, volume 3, issue 8, august 2013 1 issn 2250-3153 [www.ijsrp.org](http://www.ijsrp.org) design and analysis of stepped impedance microstrip

**Basic Antenna Theory - Wireless**

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**S-parameter Techniques Hp Application Note 95-1**

<http://www.hp.com/go/tmappnotes> test & measurement h application note 95-1 s-parameter techniques for faster, more accurate network design

**Fully Differential Op Amps Made Easy - ?????**

sloa099 fully differential op amps made easy 5  $v_{out+}$   $v_{out-}$   $v_{in}$  figure 6. relationship between  $v_{in}$ ,  $v_{out+}$ , and  $v_{out-}$  3.3 working with terminated inputs the design becomes more complicated if a single-ended input signal must be terminated.

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chapter 7 derating 1. introduction . 1.1 derating is a design process that can make a significant contribution to reliability. this chapter describes policies and methods for derating electronic components and mechanical systems equipment.

**Ap-5131 Access Point - Michael Mcnamara**

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